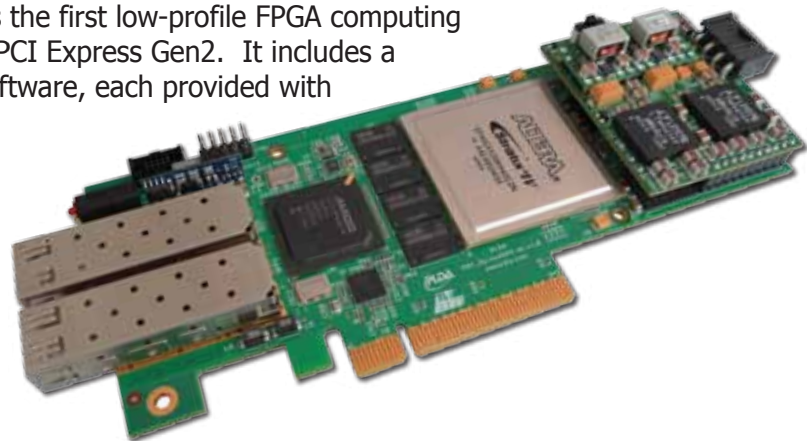


Low Profile, Dual-10G Ethernet PCI Express® Computing Card

The Accelize FPGA-based XP4530LP-20G is the first low-profile FPGA computing platform featuring dual-10G Ethernet and PCI Express Gen2. It includes a combination of hardware, firmware and software, each provided with different options to fit specific acceleration requirements.

Engineered to accelerate financial trading applications, the XP4530LP-20G features the performance and ultra-low latency necessary for high-frequency trading, smart feed handling, and real-time algorithmic trading.



Specifications & Features:

- PCI Express® low profile form factor board (half height)
 - PCI Express® Base Specification 2.0 compliant
 - RoHS compliant
- Based on Altera Stratix IV GX EP4SGX530KF40C2N
- Connectivity:
 - PCI Express® 2.0/1.1 x8, x4, x1
 - Dual 10-Gigabit or dual 1-Gigabit Ethernet
 - Dual SFP+ cage supports 10GBASE-LRM/SR/LR & 1000BASE-X and passive Direct Attach SFP+ Cable
 - Interface for timestamping module
- Memory
 - 2 independent banks of 1GB DDR3 SDRAM
 - 4 independent banks of 9MB QDR2+ SDRAM
- Power supply
 - Through PCIe edge connector
 - Through Molex 4-pin HDD connector
- Other resources
 - 3x LED
 - 3x Switches
 - 1x extended RS232

Benefits:

- Delivered with a comprehensive design environment that enables seamless application integration and boosts productivity.
- Low-profile form factor integrates into any computing platform, including 1U rackmount servers.
- Use of established FPGA technology from leading vendors provides assurance of quality and reliability.
- Dedicated technical support provided by the design engineers guarantees project success.

About Accelize:

Accelize, a PLDA GROUP company, is a global provider of FPGA-based platforms and systems for co-processing and high-performance computing applications. Accelize FPGA solutions are specifically aimed at accelerating financial trading platforms.

PLDA GROUP products and services are trusted by over 2,000 companies worldwide, providing them with a key time-to-design and time-to-market advantage.